

Fig. 1

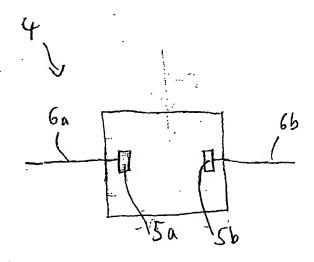


Fig. 2

Select- Signal	memory
01	soft writing
10	non-volatile writing
11	hard writing

Fig. 3

memory
mode

Selection information bits to be stored in memory component
bits

TO 11

To 10

Tig. 4